IN THE UNITED STATES PATENT AND TRADEMARK OFFICE UTILITY PATENT APPLICATION TRANSMITTAL LETTER

Attorney Docket No.: ONS00504
Mailing Date: 10/2/2003

03970 U.S. PTO 10/678769

To: Assistant Commissioner for Patents

Mail Stop Patent Application

P.O. Box 1450

Alexandria, VA 22313-1450

Dear Sir:

Re Attorney Docket No. ONS00504, transmitted herewith is an application for Nonprovisional Utility Patent, entitled

POWER SWITCH STRUCTURE WITH LOW RDSON AND LOW CURRENT LIMIT AND METHOD

for filing under 37 C.F.R. §1.53(b) as a New Patent Application.

The inventor(s) is/are

Stephen P. Robb David K. Briggs

Patent application filing papers include,

- 27 page(s) of specification and claims, and seven (7) sheets of formal drawings.
- Newly executed oath or declaration combined with power of attorney on 2 page(s).
- An assignment transmittal letter and assignment of the invention to Semiconductor Components Industries, LLC.
- Return Receipt Postcard.
- ★ The filing fee is calculated as follows:

CLAIMS AS FILED, LESS ANY CANCELED BY AMENDMENT

| CLAIMS | In Appin. | Allowed | Excess | Fee | Total Fee |
|------------------|-----------|-----------|-------------|-----------------|----------------|
| | 20 | 20 | 0 | \$18.00 | \$0.00 |
| Total | 20 | 3 | | \$86.00 | \$0.00 |
| Independent | | | | , | \$770.00 |
| Basic Filing Fee | | | | | \$770.00 |
| Total | | ooki di m | ul amount o | l a la constant | and any Extens |

- Please charge Deposit Account No. 501086 in the Total amount shown above, and any Extension Fee under 37 C.F.R. §1.136(a) if applicable.
- The Commissioner is hereby authorized to charge any additional fees which may be required now or in the future during the entire pendency of this application under 37 C.F.R. 1.16 or 37 C.F.R. 1.17, including any present or future time extension fees which may be required, or credit any overpayment to Deposit Account No. 501086.
- This sheet is submitted in duplicate.

This transmittal letter has $\underline{1}$ total pages.

Oct. 2, 2013

James Stinanu

44,358 REG. NO.

DATE

ON SEMICONDUCTOR
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